OF T	IN THE UNITED STATES PAT	ENT AND TRADEMARK	OFFIC!	E		
WE OF	Applicant(s): Ogawa, Kazuo					
PATENT & J	rial No.: 09/964,995	Group Art Unit: 2811				
	Filed: September 27, 2001	Examiner: Tran, T.				
	Title: Semiconductor Device and Manufacturing Method Therefor	Examinor. 11un, 11				•
	Attorney Docket No.: N29748500S					
	RESPONSE TO FIN	AL OFFICE ACTION				
5	Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450					
, 10	A. <u>Introductory Comments</u>					
15	The following is submitted in respons 2003. The two month Advisory Action period	e to the FINAL Office Acends <u>December 1, 2003</u> .	tion date	ed <u>Oc</u>	tobe	<u>r 1,</u>
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•	I hereby certify that this correspondence is being	F.R. §1.8	1 5 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	2800		
30	[ ] transmitted via facsimile to the United States Patent and T Date of Transmittal:				<u></u>	<del></del>
35	[X] deposited with the United States Postal Service as first c P.O. Box 1450, Alexandria VA 22313-1450.  Date of Deposit: <u>December 1, 2003</u>		o: Commiss	sioner f	for Pa	ents,
40	Typed/Printed Name: Bradley T. Sako Signature: Bradley T. Sako					

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B. <u>Amendments to the Specification</u>.

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There are no current amendments to the Specification.

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

C. <u>Amendments to the Claims</u>.

There are no current amendments to the claims.

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